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(12) **United States Patent**
Silverbrook

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(54) **HANDHELD IMAGING DEVICE
INCORPORATING MULTI-CORE IMAGE
PROCESSOR**

(58) **Field of Classification Search**

None

See application file for complete search history.

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(56) **References Cited**

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(US)

U.S. PATENT DOCUMENTS

1,960,667 A 5/1934 Hutt et al.

2,506,035 A 5/1950 Parker

(Continued)

(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
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FOREIGN PATENT DOCUMENTS

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AU 96-44491 A 8/1996

AU 55414/98 A 8/1998

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OTHER PUBLICATIONS

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(Continued)

Related U.S. Application Data

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G06K 15/02 (2006.01)
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CPC **H04N 1/00278** (2013.01); **B41J 2/14**
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(57) **ABSTRACT**

A handheld imaging device includes an image sensor for sensing an image; a processor for processing the sensed image; a plurality of processing units provided in the processor, the plurality of processing units connected in parallel by a crossbar switch to form a multi-core processing unit for the processor; and an image sensor interface for converting signals from the image sensor to a format readable by the plurality of processing units, the image sensor interface sharing a wafer substrate with the processor. A transfer of data from the image sensor interface to the plurality of processing units is conducted entirely on the shared wafer substrate.

18 Claims, 149 Drawing Sheets

